

03/9/03

EAST - (Default EAST Workspace (Flat Panel LANDSCAPE) wsp.1)

FileViewEditToolsWindowHelp

Drafts

BRS:

Pending

Active

Failed

Saved

(1) ("0709216").PN.

(232) (709/216).CCLS.

(0) (access\$3 same memory same (radio adj telephon\$3)) and 709/208-219.ccls.

(2) (access\$3 same (radio adj telephon\$3)) and 709/208-219.ccls.

(0) (access\$3 same memory same dsp same terminal) and 709/208-219.ccls.

(13) (access\$3 same memory same dsp) and 709/208-219.ccls.

(48) access\$3 same memory same (mobile or (radio adj telephon\$2)) same dsp

(92) access\$3 same memory same dsp same terminal

(269) access\$3 same shared same memory same dsp

(4) (access\$3 same shared same memory same dsp) and 709/208-219.ccls.

(29) 710/\$.ccls. and (access\$3 same shared same memory same dsp)

(26) access\$3 same shared same memory same (multi adj process\$3) same (external\$3 or terminal)

(2) processor same harvard same architecture same double

(314) processor same harvard same architecture

(241) memory same (processor same harvard same architecture)

(3) (multi adj processor) same (memory same (processor same harvard same architecture))

(3) (multi adj process\$3) same (memory same (processor same harvard same architecture))

(8) shared same (memory same (processor same harvard same architecture))

(2) (multi adj process\$3) same bridge same terminal

(198) (multi adj process\$3) same bridge

(121) (shared adj memory) same bridge

(4) ((multi adj process\$3) same bridge) same shared

(4) dsp same ((shared adj memory) same bridge)

(0) dsp same ((multi adj process\$3) same bridge)

(6) dsp same bridge same microcontroller same memory

(96) pcs1900

(1) dsp same pcs1900

(1) pcs1900 and dsp and microcontroller and memory

(17) (multi adj process\$3) and (dcs1800 or pcs1900 or gsm or ie95 or ie136)

(0) processor same (("128" adj bit) adj (data adj bus)) same bridge

(0) (("128" adj bit) adj (data adj bus)) same bridge

(72) ("128" adj bit) adj (data adj bus)

(8) (multi adj process\$3) and (("128" adj bit) adj (data adj bus))

(0) (multi adj process\$3) same ((radio adj telephon\$2) or radiotelephon\$3)

(21) (multi adj process\$3) and ((radio adj telephon\$2) or radiotelephon\$3)

(2) (multi adj process\$3) same terminal same bridge

(63) (multi adj process\$3) and (terminal same bridge)

(1) ("128" adj bit) and 6526462.ph.

Favorites

Tagged (11)

UDC

Queue

Trash

PU

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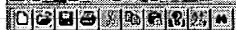
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Doc	PT	WP	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	3
1	<input type="checkbox"/>	<input type="checkbox"/>	WO 9430022 A1	19941222	85	SIGNALING PROCESSOR FOR DIGITAL		455/550;		BRIONI, MASSIMO et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>						455/550; 385			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Ready

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Drafts

BRS:
Pending

Active

L1: (0) pgm adj ram
L2: (0) (bus adj width) same dsp same terminal
L3: (37) (bus adj width) same dsp
L4: (1) (external adj3 dsp) same bridge same memory
L5: (125) (cell56 or wireless) same memory same access\$3 same dsp
L6: (106) (cellular or wireless) same memory same access\$3 same dsp
L7: (0) bridg\$3 same 16
L8: (30) bridg\$3 and 16
L9: (12) (("6230255") or ("6047358") or ("5884092") or ("5867726") or ("5794067") or ("576861...
L10: (1) ("6353863").PN.
L11: (18) xd same yd same pd
L12: (6) mtp adj memory
L13: (97) (harvard adj architecture) same dsp
L14: (1) conflict and 6526462.pn.

Failed

Saved

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U	1	PT	P	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	5
<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	WO 9430022 A1	19941222	85	SIGNALING PROCESSOR FOR DIGITAL		455/550;		BRIONI, MASSIMO et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
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Ready

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